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Bib Data Sheet

CONFIRMATION NO. 8340

SERIAL NUMBER 09/837,882	FILING DATE 04/18/2001 RULE	CLASS 714 706	GROUP ART UNIT 2133 2129	ATTORNEY DOCKET NO. M-9570 US

APPLICANTS

Christopher H. Pham, Milpitas, CA;

** CONTINUING DATA •••••••••••••••••••••••••• *mtt*

** FOREIGN APPLICATIONS ••••••••••••••••••• *mtt*

IF REQUIRED, FOREIGN FILING LICENSE
GRANTED ** 06/11/2001   *mtt*

Foreign Priority claimed ☐ yes ☒ no				
35 USC 119 (a-d) conditions met ☐ yes ☒ no ☐ Met after Allowance	STATE OR COUNTRY CA	SHEETS DRAWING 3	TOTAL CLAIMS 42	INDEPENDENT CLAIMS 4
Verified and Acknowledged *Examiner's Signature* *Initials*				

ADDRESS

~~Jack V. Musgrove~~

SKJERVEN MORRILL MacPHERSON LLP
Suite 700
25 Metro Drive
San Jose ,CA 95110-1349

TITLE

Linear associative memory-based hardware architecture for fault tolerant ASIC/FPGA work-around

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